



Hardware Selection:

•<u>Preliminary Considerations</u> - contains several pointers to considerations you should take into account before moving on finding the right hardware.

•<u>Hardware Components</u> - contains information on the different components that makes up a hardware solution and what impact they have on a TouchGFX application.

Introduction

Goal of this presentation

- Provide information to help developers with selecting hardware for graphcis applications
 - Preleminary considerations for how the application can have an impact on the needed hardware
 - · Information on the hardware components that an embedded GUI solution consist of
- Guide developers in the Board Bring Up phase, thereby helping with setting up the neccesary parts for an GUI solution and ensuring the drivers work





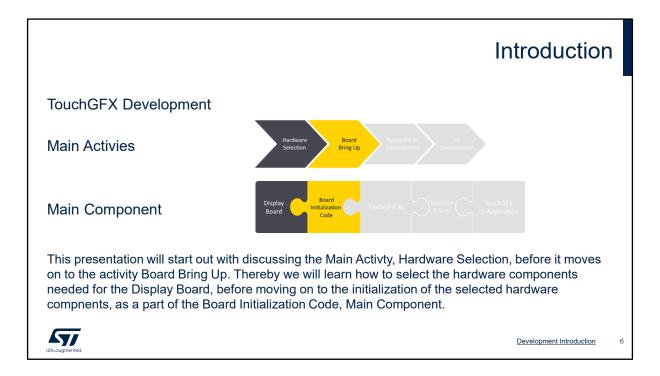
Introduction

5

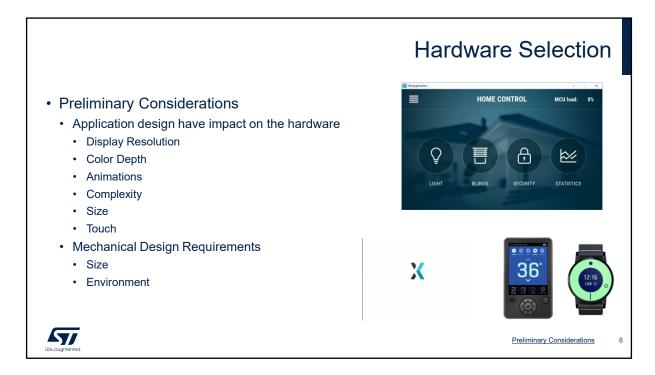
Further reading

- More help and information can be found at the TouchGFX documentation site: <u>https://support.touchgfx.com/</u>
- Slides in this presentation will refer to relevant documentation pages. Links will be in the lower right-hand corner of the slides
- The presentation will general be related to the two sections: <u>Hardware Selection</u> & <u>Board Bring Up</u>





Hardware Selection

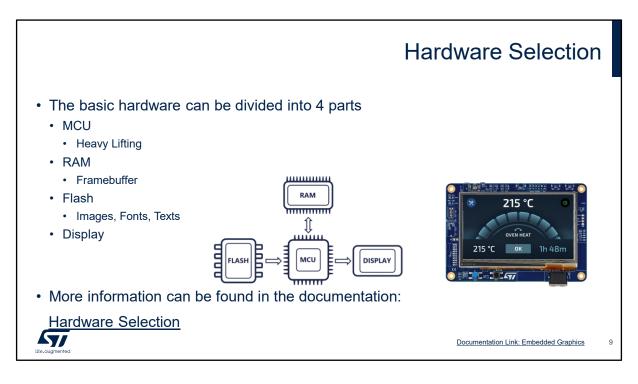


Do you need high speed full screen transitions?

Do you need complex texture mapper animations like rotation and scaling?

Display Resolution = Framebuffer size

Presentation Embedded Graphics - Basic Concepts



The Main parts

Flash

Static data Stores the image, Fonts and texts

Ram

Mainly for framebuffer, which is where the calculated image is stored, and transferd from, to the display

Display

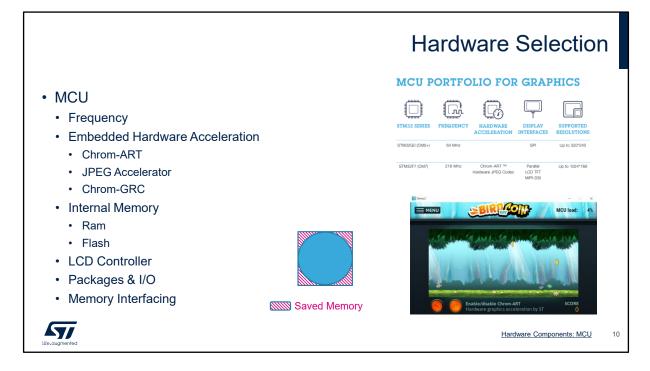
Image from Ram (sizes, types, resolution)

MCU

Handles the heavy lifting, calculates the color, renders transfer images

Muliple set up with internal ram, flash, displays

Example: H750, external flash, ram, LTDC display



Notes:

Before board bring-up, we need HW, this section wil help by understanig which hardware to chose based on the end application.

Use an STM32 DK or Eval as reference when doing the first pair of prototypes

Frequency:

Graphic Subsystem Frequency = internal busses, the frequency of the graphics accelerator and internal and external memories.

LCD Controller

High resolution = high speed interface RGB-TFT and MPI-DSI interfaces are often used for higher resolutions, as the bandwidth is in many cases higher than SPI or parallel 8080/6800.

Memory Interfacing

STM32 offers different types of memory controllers to interface with different types of external flash and ram

LCD-TFT - thin-film-transistor, variant of LCD, common used type of display, and is also the most used on oure example kits MIP – memory in Pixels,

NOR Flash Memory mapped mode external flash is seen as an internal memory for read operations.

NAND Flash

high volume of graphical assets and faster write and erase operations. cannnot be configured in a memory mapped mode. not recommended for code execution. Using a cache in RAM is often necessary when using NAND flash.

eMMC eMMC (Embedded Multi Media Card) provides standard interfaces and management for the flash memory

RAM:

- •Density
- •Performance
- •Power consumption
- Interface / pin size
- Framebuffer strategy

SRAM

SRAM typically has a faster access time compared to DRAM and is therefore more suitable for GUIs

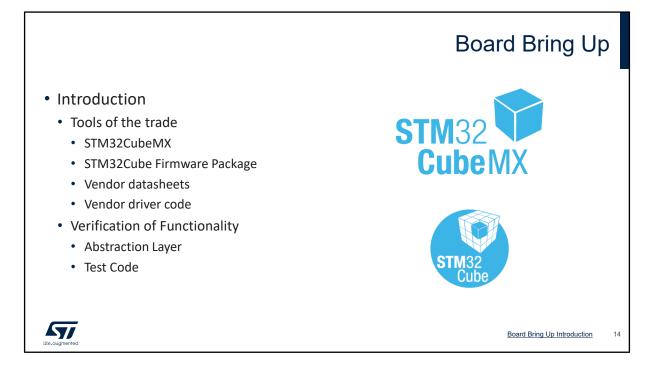
SDRAM

physical space to store the same amount of data compared to SRAM it requires more power compared to SRAM

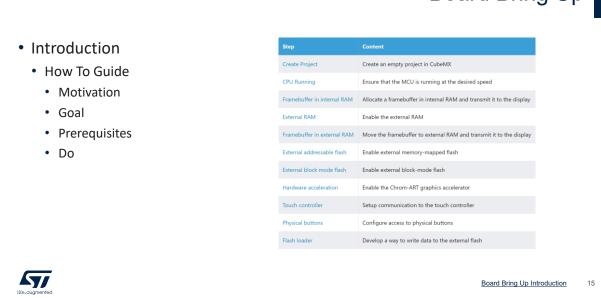
PSRAM

PSRAM compared to traditional SDRAM and SRAM has the advantages of higher speed and lower power consumption.

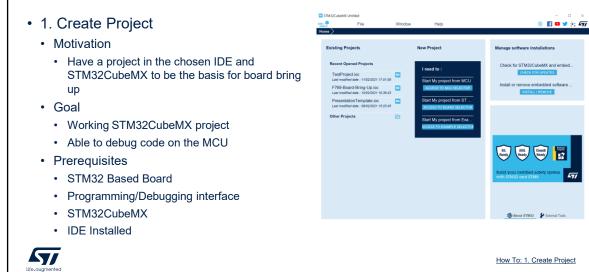




STM32CubeMX is a graphical tool that allows a very easy configuration of STM32 microcontrollers and microprocessors, as well as the generation of the corresponding initialization Code



Motivation = Why Goal = what expect to be done when finished



• 1. Create Project

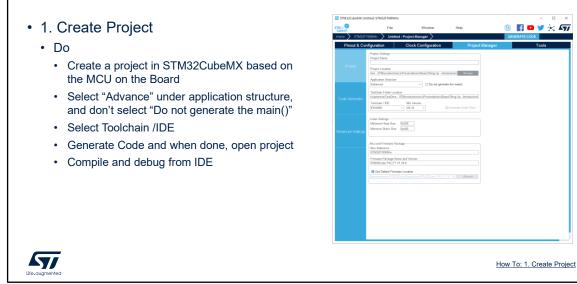
• Do

57

- Create a project in STM32CubeMX based on the MCU on the Board
- Select "Advance" under application structure, and don't select "Do not generate the main()"
- Select Toolchain/IDE
- · Generate Code and when done, open project
- Compile and debug from IDE

🔺 🔂 🗖	20	-	Features	Block Diagram	Docs					📑 Buy	
Part Number		÷ [STM32F7 Series								
lore	,			High-perfor	manc	e and	DSP with	FPU, Arr	n Corte	K-M7	
Series	>		STM32F769N	Accelerator	High-performance and DSP with FPU, Arm Cortex-M7 MCU with 2 Mbytes of Flash memory, 216 MHz CPU, Art Accelerator, L1 cache, SDRAM, TFT, MIPI-DSI, JPEG codec, DFSDM						
ine	>										
			ACTIVE Active		Unit Price for 10kU (USS) : 8.939			·			
Package	>		Product is in mass	Boards: STM32	Boards: STM32F769LDR		SC1-		TEBCA216		
ther	· · · ·		production	STM32F769I-EV	STM32F769I-DISCO - STM32F769I-EVAL			19034210			
			ha hinh narformani	a Arm [®] Cortav® b	17 99 h	* 010/7	rore onerali	na at un to	716 Mile		
			MPUs List: 1804 iter	15		+ Dis	play similar i	ems		🕂 Export	
				15	Mark	+ Dis	play similar i Board X 1	ems	w X x	Export	
		MCUs/	MPUs List: 1804 iter Dart No. 7 StimuZi Tior VO	ns Reference	Mark	+ Dis 6.677	play similar i Board 2 11 102	iems ash X RA	M X K By 82	> × Freq. ×	
		MCUs/	MPUs List: 1804 iter	ns Reference STM32F767VGHx	Mark-	+ Dis 6.677	play similar i Doard 20 11 102 204	ems 4 kB 512 i	м × к Юу 82 Юу 82	216 MHz	
		MCUs/	MPUs List: 1804 iter Dart No. 7 StimuZi Tior VO	ns STM32F767VGHx STM32F767VITx STM32F767VIHx	Mark Active Active		play similar 8 Bound 12 102 204 204	ems 4 kB 512 i 8 kB 512 i	M × × H3y 82 H3y 82 H3y 82	216 MHz 216 MHz	
		MCUs/ 1 1 1 1 1 1 1 1 1 1 1 1 1	MPUs List: 1804 iter Statistic for VS STM32F767V1	ns STM32F767VGHx STM32F767VITx STM32F767VIHx	Active Active Active	+ Dis 6.577 7.428 7.428 7.109	play similar 7 (00ard >> 102 204 204 102	erms 4 kB., 512 i 8 kB., 512 i 8 kB., 512 i	M X K By 82 By 82 By 82 By 114	216 MHz 216 MHz 216 MHz 216 MHz	
		MCUs/ 1 1 1 1 1 1 1 1 1 1 1 1 1	MPUs List: 1804 Ren STIMS2F767V STIM32F767V1 STIM32F767ZG	THE STATES OF TH	Active Active Active Active		0 lay similar 8 100ard 12 102 204 204 102 NUCLE 204	ems 4 kB 512 i 8 kB 512 i 8 kB 512 i 4 kB 512 i	M X K Harris K Karris K K K K K K K K K K K K K K	216 MHz 216 MHz 216 MHz 216 MHz 216 MHz 216 MHz	
		MCUs/	WPUs List: 1804 Rec STM32F767VI STM32F767ZG STM32F767ZG STM32F767ZJ	Reference STM32F767VGHz STM32F767VITx STM32F767VHz STM32F767ZGTx	Active Active Active Active Active Active	+ Dis 6.577 7.428 7.428 7.428 7.109 7.96 7.024	0 lay similar 8 102 204 204 102 NUCLE 204 204	ems 4 kB 512 i 8 kB 512 i 8 kB 512 i 4 kB 512 i 8 kB 512 i 8 kB 512 i	M X 82 (By 82 (By 82 (By 82 (By 114 (By 114 (By 128	216 MHz 216 MHz 216 MHz 216 MHz 216 MHz 216 MHz 216 MHz	
		MCUs/ A A A A A A A A A A	MPUs List: 1804 Rer STM32F767VI STM32F767VI STM32F767ZG STM32F767ZI STM32F765AI	Reference STM 32F 767VGHz STM 32F 767VHz STM 32F 767VHz STM 32F 767VHz STM 32F 767CGTx STM 32F 769DGTx STM 32F 769DGTx STM 32F 769DGTx	Active Active Active Active Active Active	+ Dis 6.577 7.428 7.428 7.428 7.109 7.96 7.024 7.875	Diay similar 8 000440 20 204 204 204 204 204 204 204 2	erms 4 kB 512 1 8 kB 512 1	M X 82 (By 82 (By 82 (By 114 (By 114 (By 128 (By 159 (By 159	216 MHz 216 MHz 216 MHz 216 MHz 216 MHz 216 MHz 216 MHz 216 MHz	
			MPUs List: 1804 iter STM32F767V STM32F767ZG STM32F767ZG STM32F765A STM32F769A STM32F769A	5 815/0325767VGHx STM325767VFx STM325767VFx STM3257672VFx STM3257672VFx STM3257672VFx STM3257690Fx STM3257690Fx STM3257690Fx STM3257690Fx	Mark Active Active Active Active Active Active Active Active Active		lay similar 8 0000012 102 204 102 NUCLE 204 102 204 102 204 102	erns 4 HB 512 8 HB 512	49 x 82 49, 82 49, 82 49, 12 49, 114 49, 128 49, 129 49, 159 49, 131	 X Freq. X 216 MHz 	
			MPUs List: 1804 Ren STM28/7674 STM28/7674 STM32/7674 STM32/76764 STM32/76960 STM32/7696 STM32/7696	Beforence STM322*767VGHz STM322*767VGHz STM322*767VGHz STM325*7672GHz STM325*7672GHz STM325*7672GHz STM325*7672GHz STM325*7690GHz STM325*7690GTz STM325*7690GTz STM325*7690GTz	More Active Active Active Active Active Active Active Active Active Active	+ Dis 6.677 7.428 7.428 7.109 7.96 7.024 7.875 8.726 7.449 8.301	lay similar 8 (loand 32 51 102 204 204 204 204 204 204 204 204 204 2	erns ann X 20 4 kB512 i 8 kB512 i	4 X 82 49, 82 49, 82 49, 82 49, 114 49, 128 49, 128 49, 129 49, 159 49, 131	216 MHz 216 MHz	
			MPUs List: 1804 Rec STM02F767V STM02F767V STM02F767A STM02F767A STM02F769A STM02F769A STM02F769A STM02F769A STM02F769A STM02F769A	Beforence STM326767VGHz STM326767VFbz STM326767VFbz STM326767VFbz STM3267672GTx STM32676762GTx STM32676762GTx STM32677672GTx STM32677672GTx STM32677672GTx STM32677690GTx STM32677690GTx STM32677690GTx STM32677690HTx STM32677690HTx STM3267690GTx STM3267690HTx STM36767690HTx STM36767690HTx STM36767690HTx<	Active Active Active Active Active Active Active Active Active Active Active Active	+ Dis 6.677 7.428 7.428 7.109 7.96 7.024 7.875 8.726 7.449 8.301 8.088	alay similar a 0000005 11 204 204 204 204 204 204 102 204 102 204 102 204 102 204 102 204 102 204	erros anto 2 00 4 kB 512 i 8 k	By 82 By 82 By 82 By 114 By 114 By 128 By 159 By 131 By 159	216 MHz 216 MHz	
			MPUs List: 1804 Red STM22F767V STM22F767V STM22F767A STM22F769A STM22F769A STM22F769A STM22F769A STM22F769A STM22F769A	Reference STM325767VGHz STM325767VGHz STM325767VHz STM325767VHz STM325767VHz STM325767VHz STM325767VHz STM325767VHz STM3257670HTz STM3257690HTz STM3257690HTz STM3257690HTz STM3257690HTz STM3257690HTz STM3257690HTz STM3257090Htz	Active Active Active Active Active Active Active Active Active Active Active Active Active Active Active		0 lay similar 10 (1004013) 11 1022 204 204 204 102 204 102 204 102 204 102 204 204	Bits Bits a kb 512 i k kb 512 i	M X M (By	216 MHz 216 MHz	
			MPUs List: 1804 Rec STM02F767V STM02F767V STM02F767A STM02F767A STM02F769A STM02F769A STM02F769A STM02F769A STM02F769A STM02F769A	Beforence STM326767VGHz STM326767VFbz STM326767VFbz STM326767VFbz STM3267672GTx STM32676762GTx STM32676762GTx STM32677672GTx STM32677672GTx STM32677672GTx STM32677690GTx STM32677690GTx STM32677690GTx STM32677690HTx STM32677690HTx STM3267690GTx STM3267690HTx STM36767690HTx STM36767690HTx STM36767690HTx<	Active Active Active Active Active Active Active Active Active Active Active Active Active Active Active	+ Dis 6.677 7.428 7.428 7.109 7.96 7.024 7.875 8.726 7.449 8.301 8.088	0 lay similar 1 (000013% 01 2044 2044 102 2044 2044 102 2044 102 2044 102 2044 102 2044 102 2044 102 2044 102 2044 102 102 102 102 102 102 102 102	erros anto 2 00 4 kB 512 i 8 k	(B)	216 MHz 216 MHz	

How To: 1. Create Project 17

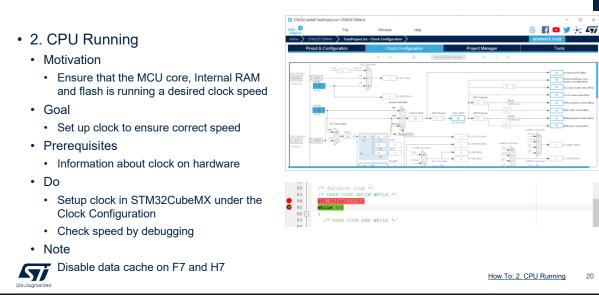


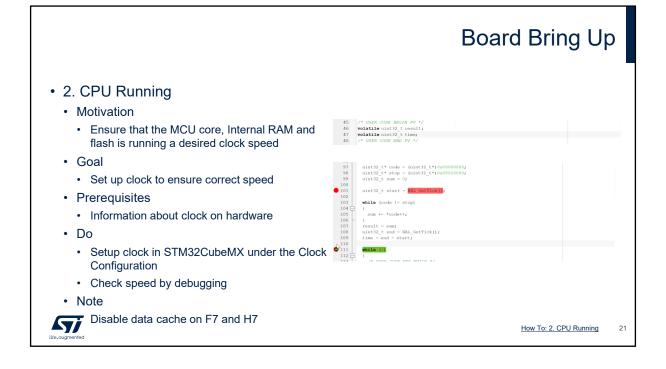
• 1. Create Project

- Do
 - Create a project in STM32CubeMX based on the MCU on the Board
 - Select "Advance" under application structure, and don't select "Do not generate the main()"
 - Select Toolchain /IDE
 - · Generate Code and when done, open project
 - Compile and debug from IDE

lorkspace	• 0 ×			Ŧ	Disassembly • a ×
lestProject		main()		fo	Go to
Files	۰.	+ 63 64 -	int main (woid)	^	Disassembly ^
TestProject - TestProj	~	65	/* USER CODE BEGIN 1 */		0x800088e: 0:
- Application		66			0x8000892: 0:
- Drivers		67	/* USER CODE END 1 */		SysTick->VAL
- 🗉 📫 Output		60			0x8000896: 0:
		69	/* MCU Configuration		SysTick->CTRL
		70			
		71	/* Reset of all peripherals, Initializes the F	245	
		72	HAL_Init();		0x8000898: 0:
		73			0x800089a: 0:
		74	/* USER CODE BEGIN Init */		return (OUL);
		75			0x800089c: 0:
		76	/* USER CODE END Init */	- 10	0x800089e: 0
		77		- 10	0x80008a0: 0:
		78	/* Configure the system clock */	- 10	0x80008a2: 0:
		79	SystemClock_Config();	- 10	0x80008a4; 0:
		80			0x80008a8: 0:
		81	/* USER CODE BEGIN SysInit */		0x80008ac: 0
		82		_	0x80008aC: 0:
		83 84	/* USER CODE END SysInit */		0x80008b4: 0:
		84			
		85	/* Initialize all configured peripherals */ /* UNER CODE BEGIN 2 */		aeabi_ldiv0: 0x80008b8: 0
		87	/* USER CODE BEGIN 2 */		
		87	/* USER CODE END 2 */		0x80008ba: 0:
		10	/* USER CODE END 2 */		int main(void)
		50	/* Infinite loop */		<
		90	/* INTINICO ICOP #/		nain:
		91	while ()		0x80008bc: 0
		93 E			HAL_Init():
		94	(/* DEER CODE END WHILE */		0x80008be: 0:
		95	/ - DIER CODE END WEILE -/		SystewClock_Com
		93 96	/* USER CODE BEGIN 3 */		0x80008c2: 0:
		97 -	/* DEER CODE BEGIN 3 */		while (1)
exProject		1			
					,
wg Log					▼ # X
Log					A . A
Thu Feb 11, 2021 14:28:35:					
Thu Feb 11 2021 14:28:35 Thu Feb 11 2021 14:28:35			segy u was performed		*
This Peeb 11, 2121, 1472, 35	I proset re-se	*1) ×
ild Debug Log					
					Ln 92, Col 1





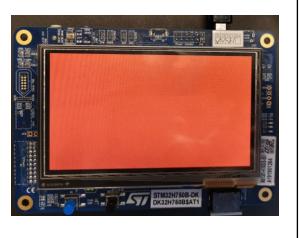


• 3. Framebuffer in internal RAM

Motivation

- Setting up an internal framebuffer and the display, enables us to have simple graphic on the display
- Goal
 - Having internal RAM allocated for the framebuffer, and show it's content on the display
- Prerequisites
 - · Information about the Display
 - Information about connection between the MCU and Display





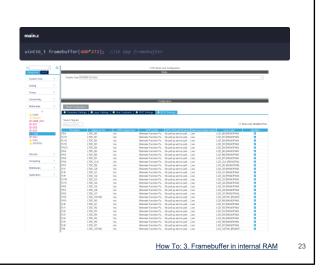
How To: 3. Framebuffer in internal RAM 22

• 3. Framebuffer in internal RAM

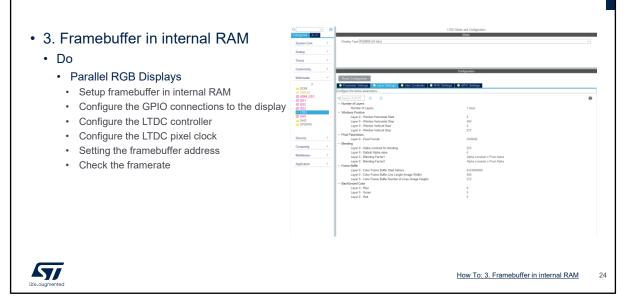
• Do

Parallel RGB Displays

- Setup framebuffer in internal RAM
- Configure the GPIO connections to the display
- Configure the LTDC controller
- Configure the LTDC pixel clock
- Setting the framebuffer address
- Check the framerate





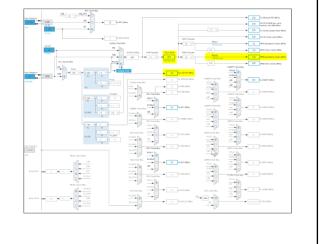


• 3. Framebuffer in internal RAM

• Do

Parallel RGB Displays

- Setup framebuffer in internal RAM
- Configure the GPIO connections to the display
- Configure the LTDC controller
- Configure the LTDC pixel clock
- Setting the framebuffer address
- Check the framerate





How To: 3. Framebuffer in internal RAM 25

• 3. Framebuffer in internal RAM

• Do

Parallel RGB Displays

- Setup framebuffer in internal RAM
- Configure the GPIO connections to the display
- Configure the LTDC controller
- Configure the LTDC pixel clock
- Setting the framebuffer address
- Check the framerate

/* USER CODE BEGIN 2 */ HAL_LTDC_SetAddress(&hltdc, framebuffer, LTDC_LAYER_1);

main.c

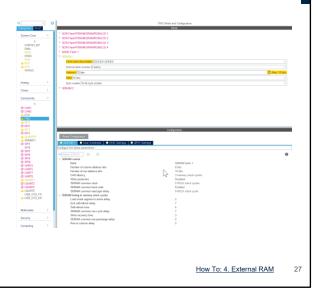


How To: 3. Framebuffer in internal RAM 26

• 4. External Ram

- Motivation
 - External RAM is often required since the framebuffer(s) does not fit in internal RAM
- · Goal
 - Enable External RAM and be able to r/w
- · Prerequisites
 - Information about the RAM
 - Information about connection between the MCU and RAM
- Do
 - Setup in STM32CubeMX
 - Some RAMs needs extra C code





4. External Ram

- Motivation
 - External RAM is often required since the framebuffer(s) does not fit in internal RAM
- · Goal
 - Enable External RAM and be able to r/w
- · Prerequisites
 - Information about the RAM
 - Information about connection between the MCU and RAM
- Do
 - Setup in STM32CubeMX
 - Some RAMs needs extra C code





Poord Pring LIn

• 4. External Ram

- Motivation
 - External RAM is often required since the framebuffer(s) does not fit in internal RAM
- Goal
 - · Enable External RAM and be able to r/w
- Prerequisites
 - Information about the RAM
 - · Information about connection between the MCU and RAM
- Do
 - Setup in STM32CubeMX
 - Some RAMs needs extra C code



Board Bring Up	
/* INF SIGNA workrail andfiguration */ Streamhardte.lini:SBank * PKC_SDRAM_ENNE2; /* Now addreaming: (1/10) */ Streamhardte.lini:GoummilteNumber * PKC_SDRAM_COLORE_BITE_HOM_B1; /* Column addreaming: (1/10) */ Streamhardte.lini:GoummilteNumber * PKC_SDRAM_HOM_BITE_HOM_B1; Streamhardte.lini:GoummilteNumber * PKC_SDRAM_HOM_BITE_HOM_CA; Streamhardte.lini:GoummilteNumber * PKC_SDRAM_HOM_BITE_HOM_CA; Streamhardte.lini:GoummilteNumber * PKC_SDRAM_HOM_BITE_HOM_CA; Streamhardte.lini:GoummilteNumber * PKC_SDRAM_HOM_BITE_HOM_CA; Streamhardte.lini:GoummilteNumber * PKC_SDRAM_HOM_BITE_HOM_CA; Streamhardte.lini:GoummilteNumber * STREAM_HOM_BITE_HOM_CA; Streamhardte.lini:GoummilteNumber * STREAM_HOM_BITE_HOM_CA; Streamhardte.lini:RestificeDecision * STREAM_HOM_BITE_HOM_CA; Streamhardte.lini:RestificeDecision * STREAM_HOM_BITE_HOM_CA; Streamhardte.lini:RestificeDecision * STREAM_HOM_BITE_HOM_CA; Streamhardte.lini:RestificeDecision * STREAM_HOM_BITE_HOM_CA; Streamhardte.lini:RestificeDecision * STREAM_HOM_BITE_HOM_CA; Streamhardte.lini:RestificeDecision * STREAM_HOM_CA; Streamhardte.lini:RestificeDecision * S	
/* SDRAM controller init Memory 1 X	
/*	
//write external RAM for(int i = 0; i < size; i++) { externalRAM[i] = i; }	
How To: 4. External RAM	29

• 5. Framebuffer in external RAM

- Motivation
 - Getting graphics on the display from External RAM
- · Goal
 - Remove Framebuffer from Internal RAM, and have the Framebuffer in external RAM
- · Prerequisites
 - Address of the framebuffer in the external RAM
- Do
 - Place the framebuffer in external RAM
 - Setup the display controller to read from the external RAM



wint14 (* framebuilfer - (wint16 (*))#Commons; //Ito hop framebuilfer main: /* user coor arcou : ۲ (*) * user coor arcou : ۲ (*) * user coor arcou : ۲ (*) * user coor in a : *) <u>How To: 5. Framebuilfer in external RAM</u> 30

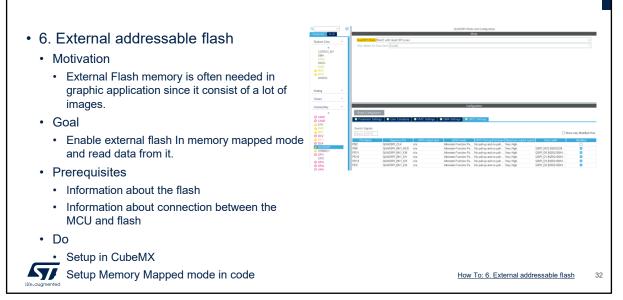
ain.c

• 5. Framebuffer in external RAM

- Motivation
 - Getting graphics on the display from External RAM
- Goal
 - Remove Framebuffer from Internal RAM, and have the Framebuffer in external RAM
- · Prerequisites
 - · Address of the framebuffer in the external RAM
- Do
 - Place the framebuffer in external RAM
 - Setup the display controller to read from the external RAM



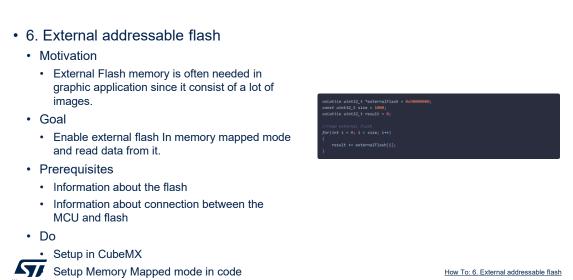
<hr/>	•	LTDC Mode and Configuration
System Core	Display Type RG8008 (24 bits)	MOSE
	>	
Analog	<u>,</u>	
limers		
Connectivity		Configuration
Multimedia	Reset Configuration	
•	Parameter Settings O Layer Settings O User Constants O NVIC:	Settings 🛛 👄 GPIO Settings
DCMI DMA2D	Configure the below parameters :	
Ø HDMI CEC	Q Search (CdH-F) 0 0	
Ø I2S1		
@ 1252	V Number of Layers	
@ 1283	Number of Layers	1 layer
V LTDC 2 SAI1	 Windows Position 	0
SAI2	Layer 0 - Window Horizontal Start Layer 0 - Window Horizontal Stop	480
SPDIFRX	Layer 0 - Window Pronzontal Stop Layer 0 - Window Vertical Start	400
		272
	Layer 0 - Window Vertical Stop V Pixel Parameters	272
curity		
icuity	Layer u - Posei Pormat	RG8565
amouting	V Blending	255
in provid	Layer 0 - Alpha constant for blending	255
iddleware	Layer 0 - Default Alpha value	
0000	Layer 0 - Blending Factor1	Alpha constant x Pixel Alpha
polication	Layer 0 - Elending Factor2	Alpha constant x Pixel Alpha
pprostor	Frame Buffer	
	Layer 0 - Color Frame Buffer Start Adress	DxC0000000
	Layer 0 - Color Frame Buffer Line Length (Image Width)	480
	Layer 0 - Color Frame Buffer Number of Lines (Image Height)	272
	BackGround Color	
	Layer 0 - Blue	0
	Layer 0 - Green	0
	Laver 0 - Red	0



• 6. External addressable flash

- Motivation
 - External Flash memory is often needed in graphic application since it consist of a lot of images.
- Goal
 - Enable external flash In memory mapped mode and read data from it.
- · Prerequisites
 - · Information about the flash
 - Information about connection between the MCU and flash
- Do
 - Setup in CubeMX
- Setup Memory Mapped mode in code

OSPI MemoryMappedTypeDef :	s_command;
QSP1_Hellor yeapped rypeber is	s_mem_mapped_crg;
s_command.InstructionMode	 QSPI_INSTRUCTION_1_LINE;
	= QUAD_INOUT_FAST_READ_CMD;
s_command.AddressMode	<pre>= QSPI_ADDRESS_4_LINES;</pre>
s_command.AddressSize	= QSPI_ADDRESS_24_BITS;
s_command.AlternateByteMod	de = QSPI_ALTERNATE_BYTES_NONE;
s_command.DataMode	
	= N25Q128A_DUMMY_CYCLES_READ_QUAD;
	<pre>= QSPI_DDR_MODE_DISABLE;</pre>
s_command.DdrHoldHalfCycle	<pre> e = QSPI_DDR_HHC_ANALOG_DELAY; </pre>
<pre>- /* Configure the memory me s_mem_mapped_cfg.TimeOutAc</pre>	= GSF_SIO_UNST_UVERV_CMD; zpred node */ tivation = GSFI_TIPECOUT_COUNTER_DISABLE; (GSOFUndle, &s command, &s men mapped cfg) != HAL OK)
<pre>- /* Configure the memory me s_mem_mapped_cfg.TimeOutAc</pre>	
<pre> Configure the memory m s_mem_mapped_cfg.TimeOutAc if (HAL_QSPI_MemoryMapped({</pre>	
if (HAL_QSPI_MemoryMapped)	
<pre> Configure the memory m s_mem_mapped_cfg.TimeOutAc if (HAL_QSPI_MemoryMapped({</pre>	
<pre> Configure the memory memory mem_mapped_cfg.TimeOutAc if (HAL_QSPI_MemoryMapped({</pre>	
<pre> Configure the memory memory mem_mapped_cfg.TimeOutAc if (HAL_QSPI_MemoryMapped({</pre>	
<pre> Configure the memory memory mem_mapped_cfg.TimeOutAc if (HAL_QSPI_MemoryMapped({</pre>	
<pre> Configure the memory memory mem_mapped_cfg.TimeOutAc if (HAL_QSPI_MemoryMapped({</pre>	
<pre> Configure the memory memory mem_mapped_cfg.TimeOutAc if (HAL_QSPI_MemoryMapped({</pre>	



• 7. External flash in block mode

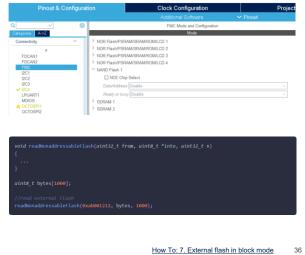
- Motivation
 - Non-Memory-Mapped Flash memory requires a driver to work with TouchGFX
- · Goal
 - A driver that can read from a location in the flash memory and store it in an array.
- Prerequisites
 - · Information about the flash
 - Information about connection between the MCU and flash
 - · The flash speed



How To: 7. External flash in block mode 35

• 7. External flash in block mode

- Do
 - Configure the NAND flash via STM32CubeMX via the FMC
 - Set the QSPI flash, similar to memory-mapped
 Including GPIO
 - Add code to read from an address of the flash

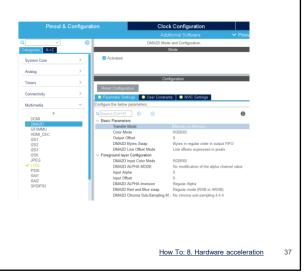


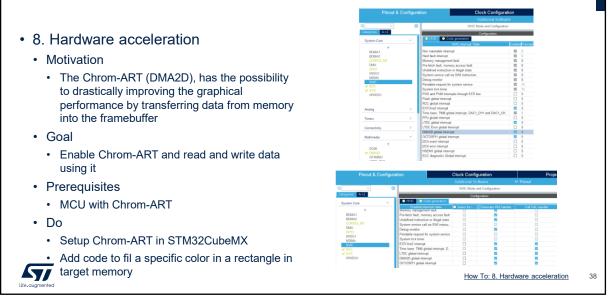


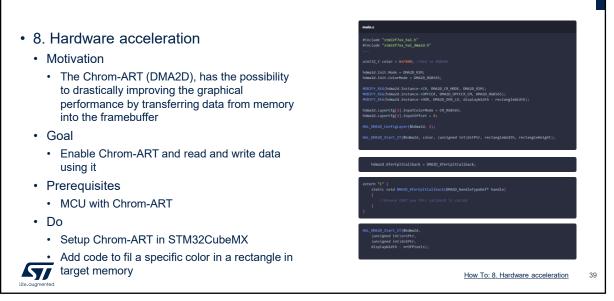
• 8. Hardware acceleration

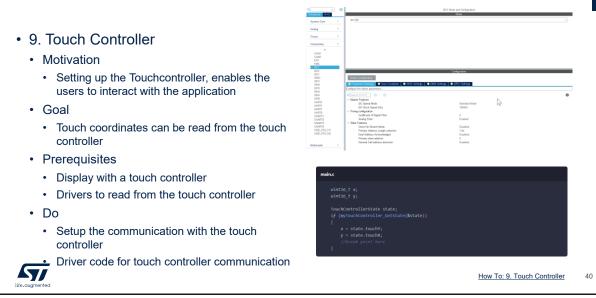
- Motivation
 - The Chrom-ART (DMA2D), has the possibility to drastically improving the graphical performance by transferring data from memory into the framebuffer
- Goal
 - Enable Chrom-ART and read and write data
 using it
- Prerequisites
 - MCU with Chrom-ART
- Do
 - Setup Chrom-ART in STM32CubeMX
 - Add code to fil a specific color in a rectangle in

target memory









<u>3V</u>3

USER WAKE-UP BUTTON

C90

B2

R11 100K

-PUSH-TS-02H-Blue

4

ALC6V1-1U2

USER BUTTON

WAKEUP

PC13

100nF should be place close to the MCU 10pF and 1K should be place close to the

• 10. Physical Buttons

- Motivation
 - Physical buttons can function as an external events to interact with the application, or as an alternative to touch
- Goal
 - Setup the application to receive input from a button
- Prerequisites
 - Physical button connected to MCU
- Do
 - Setup the GPIO for the button in STMCubeMX32
 - Add code to react on the GPIO





• 10. Physical Buttons

- Motivation
 - Physical buttons can function as an external events to interact with the application, or as an alternative to touch
- Goal
 - Setup the application to receive input from a button
- Prerequisites
 - Physical button connected to MCU
- Do
 - Setup the GPIO for the button in STMCubeMX32
 - Add code to react on the GPIO



Pin Na	Signal on .	GPIO outp.	. GPIO mode	GPIO Pull	Maximum .	Fast Mode	User Label	Modified
PA2	n/a	Low	Output Pu	Pull-up	Low	n/a	LCD_ON/	~
PA12	n/a	Low	Output Pu	No pull-up	Low	n/a	VSYNC_F	~
PB14	n/a	Low	Output Pu	No pull-up	Low	n/a	RENDER	~
PB15	n/a	Low	Output Pu	No pull-up	Low	n/a	FRAME_R	~
PC13	n/a	n/a	Input mode	Pull-down	n/a	n/a	BTN_USER	
PG2	n/a	Low	Output Pu	No pull-up	Low	n/a	LED2	V
PG11	n/s onfiguration :	I nue	Output Pu	No culluin	Low	n/a	I ED3	100
GPIO m				Input m	tode			~
GPIO P	ull-up/Pull-dow	1		Pull-do	wn			~
User La	bel			BTN_U	SER			

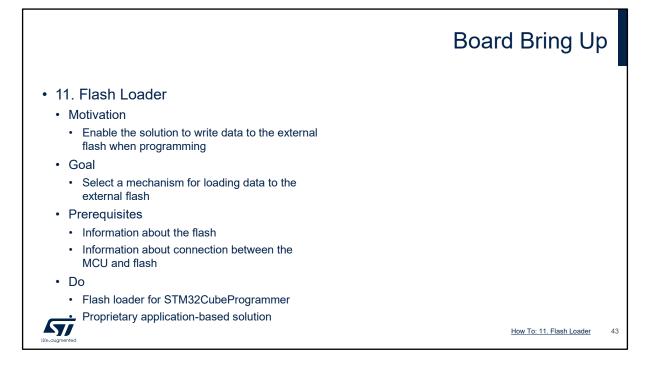
LC

rint8_t key; f (HAL_GPIO_ReadPin(GPIOC, GPIO_PIN_13) |- GPIO_PIN_RES

○ GPI0 ● I2C ● LTDC ● RCC ● NVIC

key - 1;

How To: 10. Physical Buttons 42



The compiler will compile the text, fonts, and images in your project and produce a binary or hex file with this data. This data is typically put into the external flash. The internal flash is then reserved for code.

The <u>STM32CubeProgrammer</u> comes with flash loaders for the various STM32 Evaluation kits.

The flash loader consists of two parts: Configuration of the GPIOs and flash interface that are required to communicate with the flash The flashing algorithm that knows the sequence of commands required to write in the flash

Another solution is to include flash loading into the application itself. The idea is that you already have the flash configuration inside your application (to be able to load from it),

Where to go next?

- More information can be found in documentation: <u>Hardware Selection & Board Bring Up</u>
- To get started with the Abstraction Layer Development, read the <u>TouchGFX AL Development Introduction</u>
- Or watch the presentation
 <u>Abstraction Layer Development Introduction</u>



Thank you

© STMicroelectronics - All rights reserved. ST logo is a trademark or a registered trademark of STMicroelectronics International NV or its affiliates in the EU and/or other countries. For additional information about ST trademarks, please refer to <u>www.st.com/trademarks</u>. All other product or service names are the property of their respective owners.

